DATA SHEET

PLM7922A

Linear absolute Encoder

The PLM7922 linear encoder system features a housed absolute linear encoder combined with a two-track magnetic scale, an incremental and an absolute track. The encoder delivers true power-on position detection with an effective resolution of 0.1µm.

Thanks to its robust design, the sensor is ideal for use in motion control systems, providing precise position and speed detection.

Utilizing proven MR sensor technology and integrated correction algorithms, the PLM7922 ensures high accuracy, excellent repeatability, and reliable performance even in demanding environments.

Features

- Up to 70 mm maximum measuring length
- Absolute position detection
- Effective resolution of 0.1 µm
- Absolute error smaller 20 µm
- Calibration algorithms
- True-power-on
- Wide temperature range from –40°C up to +85°C
- BISS protocol interfaces

Advantages

- Compact design
- High accuracy
- Robust and reliable

Applications

- Motion control systems
- Industrial automation
- Robotics applications
- Precision measurement

Product Overview

Article Name	Description
PLM7922-AGA-EG	Linear absolute encoder, BISS

Quick Overview

Symbol	Parameter	min.	typ.	max.	Unit
V_{CC}	Supply voltage	-	3.3	-	V
I_C	Current consumption	100	125	150	mA
Res	Resolution	-	13	-	bit
Acc	Accuracy	-	±20	-	µm
Rep	Repeatability	-	±0.5	±1.0	µm
T_{amb}	Operating temperature	-40	-	+85	°C

ESD

Mechanical Data [1)]

T_{amb} = 25°C; unless otherwise specified

Symbol	Parameter	Conditions	min.	typ.	max.	Unit
L	Length of the module		-	52.0	-	mm
W	Width of the module		-	16.0	-	mm
H	Height of the module		-	17.0	-	mm

[1)] more details in Fig. 2

Electrical Data

T_{amb} = 25°C, V_{CC} = 3.3 V; unless otherwise specified

Symbol	Parameter	Conditions	min.	typ.	max.	Unit
V_{cc}	Supply voltage		-	3.3	-	V
F_{Pos}	Position Refresh Rate		-	2.0	-	kHz
I_A	Current	V_{CC} = 5.0 V	100.0	125.0	150.0	mA
t_{Start}	Start time		-	100.0	-	ms
T_{op}	Operating temperature		-40	-	+85	°C
$T_{storage}$	Storage temperature		-40	-	+85	°C

Performance Data

T_{amb} = +25°C, V_{CC} = 3.3 V, unless otherwise specified

Symbol	Parameter	Comment	Min.	Typ.	Max.	Unit
Acc	Accuracy		-	±20.0	-	µm
Rep	Repeatability		-	±0.5	±1.0	µm
Res	Resolution	2 x Pitch	-	13	-	bit
Res_{eff}	Effective resolution		-	0.4	-	µm
Range	Measuring range	Bidirectional 3 cycles	-	16.3	-	mm

Environmental Data

Symbol	Conditions	min.	typ.	max.	Unit
Vibration resistance		-	-	785	m/s^2
Shock resistance		-	-	980	m/s^2
External magnetic field (thickness direction)		-	-	±100	mT
External magnetic field (moving direction)		-	-	±5	mT
External magnetic field (width direction)	no negative field allowed	-	-	+5	mT
Humidity		-	-	70	%

Pinout of the sensor module

Pad	Symbol	Parameter
1	IS	Inner shield
2	Clk+	Clock signal
3	Clk-	Inverted clock signal
4	V_{CC}	Supply Voltage
5	V_{CC}	Supply Voltage
6	Dat+	Data signal
7	Dat-	Inverted data signal
8	GND	Ground
9	GND	Ground

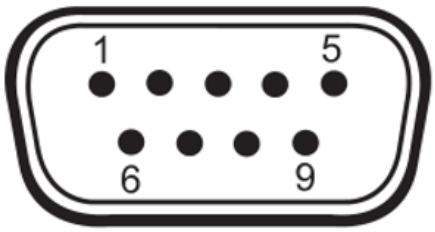


Fig. 1: Pinout of the PLM7922-AGA-EG connector
8-pin D-sub connector

Dimensions

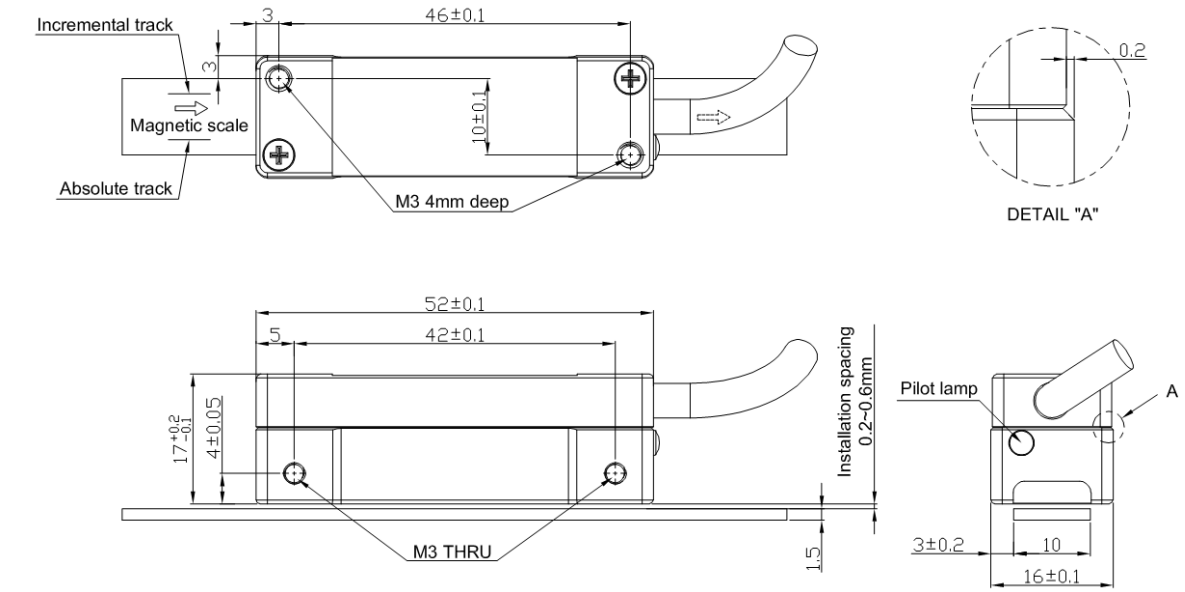


Fig. 2: Dimensions of PLM7922-AGA-EG.

Performance data

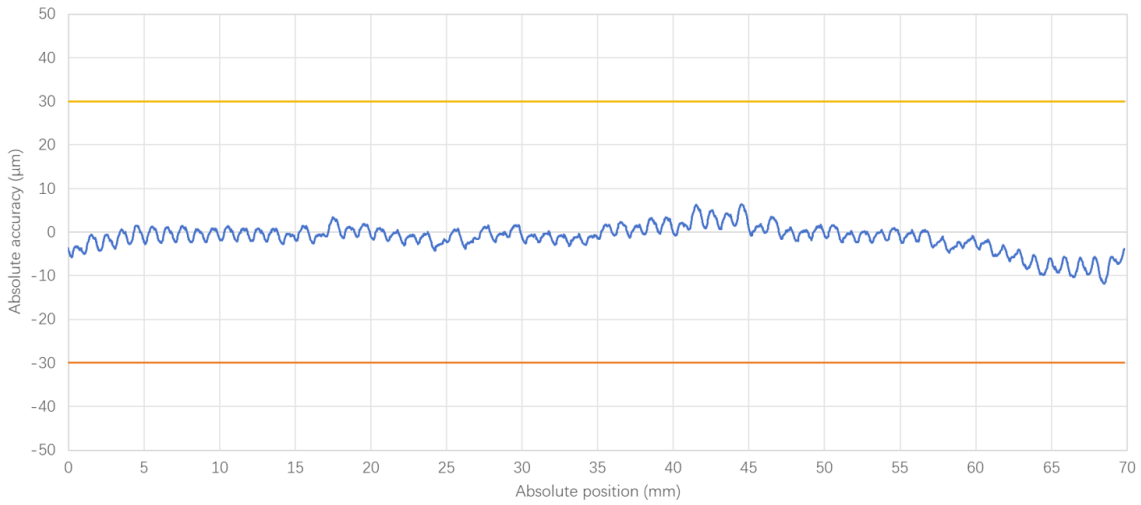


Fig. 3: Absolute accuracy

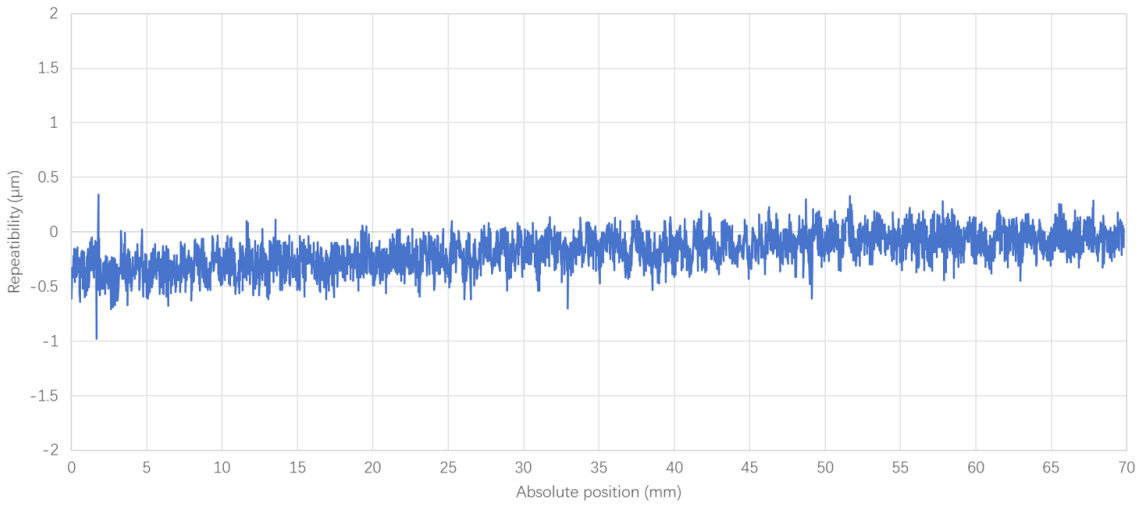
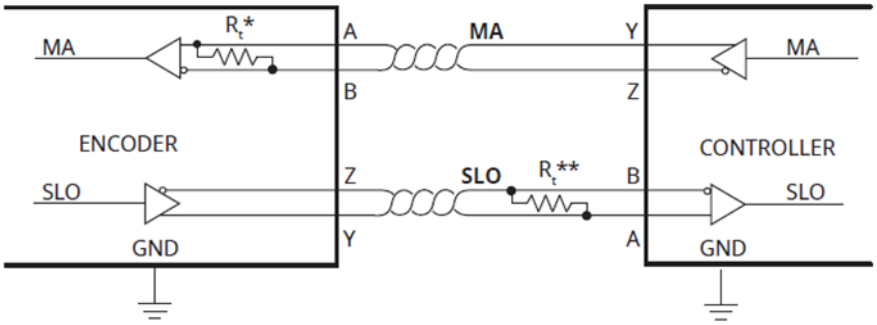


Fig. 4: Repeatibility

BISS interface

The encoder position is encoded in a 24-bit natural binary format, with data aligned to the left. Status information is provided via the BiSS C protocol. Two active-low status bits follow the position data, succeeded by an inverted CRC for data integrity.

Electrical connection



*) The MA and SLO lines are 5V RS422 compatible differential pairs. The termination resistor on the MA line is integrated inside the encoder.

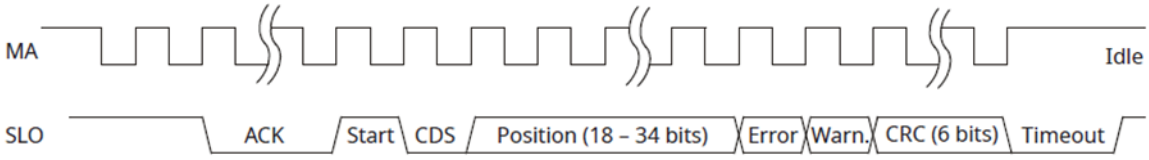
**) If the total cable length is more than 5 meters, it needs to be terminated at the controller. The cable has a nominal impedance of 120Ω.

Signal	
MA	Master clock, the maximum clock frequency is 2.5Mhz
SLO	The data is output on the rising edge of the MA

Output protection

Two mechanisms prevent excessive output current and power loss due to errors or bus collisions. Reverse current limiting on the output stage provides immediate protection against short circuits. In addition, if the chip temperature is too high, the thermal shutdown circuit forces the driver output into a high-impedance state.

Timing diagram



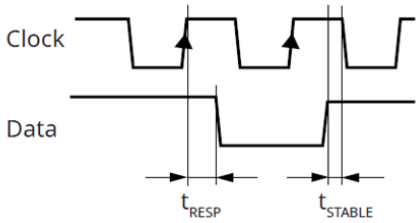
The MA is in a high state and communication starts at the first falling edge.
The encoder sets the SLO low at the second rising edge of the MA, and the ACK length is 1~30 bits.
When the encoder is ready for the next request cycle, it indicates this to the host by setting the SLO to high.
Absolute position and CRC data are provided in binary format and are first sent in MSB format.

Cable length compensation

The read head takes 170 ns to respond to the incoming clock (tRESP). The change in the data signal is delayed by 170 ns after the rising edge of the clock line. The additional delay is caused by the time it takes for the signal to propagate through the cable to the read head and back (tPROP). This delay is typically 14ns per 14 meters of cable. The total cable length from the encoder to the receiver must be considered.

Before a value can be latched, the data signal must be stable. Therefore, if the cable length is more than 1 meter and the clock frequency is higher than 1MHz, this delay must be compensated in the receiver (controller) to which the encoder is connected.

$$t_{DELAY} = t_{RESP} + t_{PROP} \times \text{cable length}$$



Status bits

Type	Value0	Value1	Description
Error	Location data is invalid	OK	Error bit activation is low. If it is low, the bit is invalid.
Warning	Location data is valid	OK	The warning bit is active low. If it is low, the encoder operation is close to its limit. The location is still valid, but the resolution and/or accuracy may be out of specification.

Communication parameters

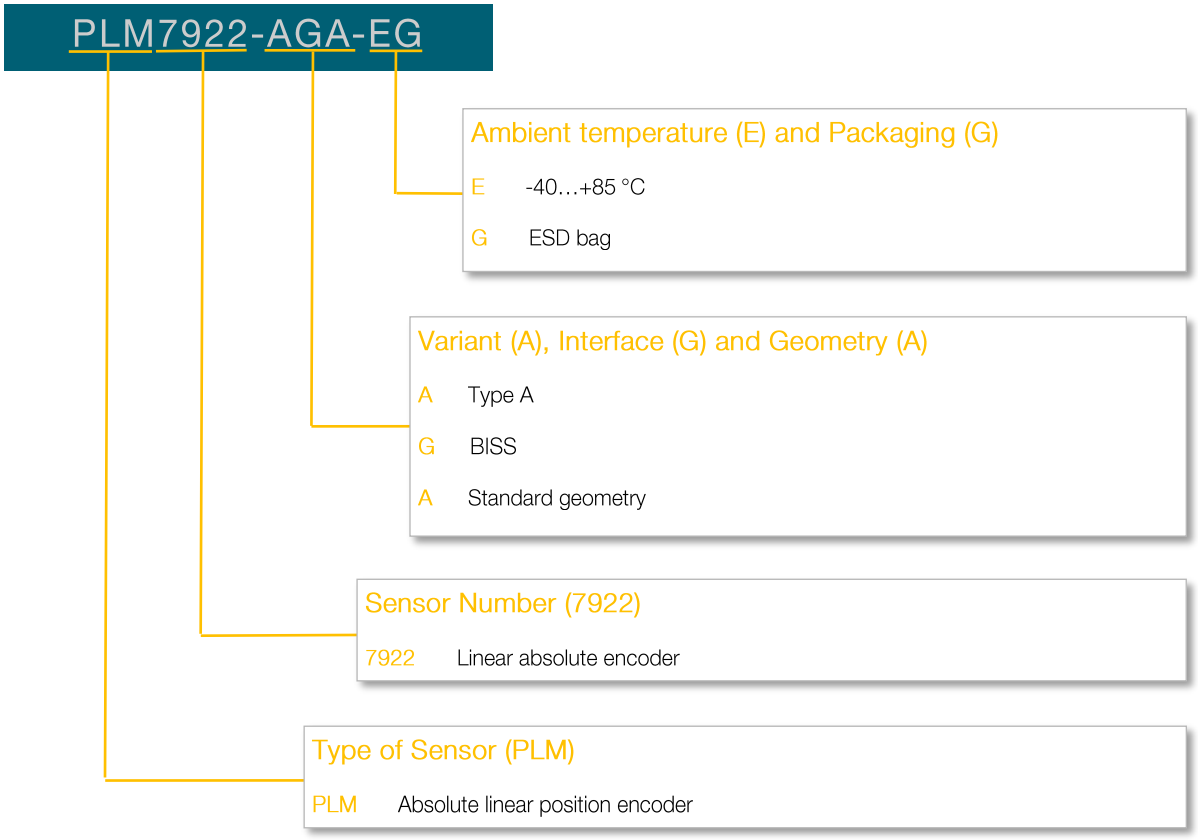
Parameter	Value
MA frequency	Max. 2.5 MHz
ACK length	1-30 bit
Register access	No

The "Bandwidth" parameter is the mechanical bandwidth. AksIM samples at 18 kHz, so the mechanical change occurs faster than 9 kHz and the output cannot be detected (Nyquist's theorem). If the position request is faster than the sampling frequency, the AksIM encoder recalculates the position at the time of the request based on the current ring velocity.

Packet description

24-bit position + 2-bit status + 6-bit CRC = 32-bit long packets.
The CRC calculation polynomial for position, error, and warning data is: x6 + x1 + 1. It is also denoted as 0x43. It is inverted and transmits the MSB first.

Additional Information on Ordering Code



General Information

Product Status

Article	Status
PLM7922-AGA-EG	The product is under development.
Note	The status of the product may have changed since this data sheet was published. The latest information is available on the internet at www.sensitec.com.

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Changelist

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PLM7922A.DSE.00	Original (pp. 1-9)	04/2025

Sensitec GmbH
Schanzenfeldstr. 2 • 35578 Wetzlar • Germany
Tel +49 (0) 6441 5291-0 • Fax +49 (0) 6441 5291-117
sensitec@sensitec.com • www.sensitec.com