

PLM7922C

Linear absolute Encoder

The PLM7922 linear encoder system features a absolute linear encoder combined with a two-track magnetic scale, an incremental and an absolute track. The encoder delivers true power-on position detection with an effective resolution of 0.1 μm .

Thanks to its robust design, the sensor is ideal for use in motion control systems, providing precise position and speed detection.

Utilizing proven MR sensor technology and integrated correction algorithms, the PLM7922 ensures high accuracy, excellent repeatability, and reliable performance even in demanding environments.





Product Overview

Article Name	Description
PLM7922-CSA-EG	Linear absolute encoder, SPI

Quick Overview

Symbol	Parameter	min.	typ.	max.	Unit
V _{CC}	Supply voltage	-	3.3	-	V
I _C	Current consumption	100	125	150	mA
Res	Resolution	-	12	-	bit
Acc	Accuracy	-	-	±30	μm
Rep	Repeatability	-	±0.5	-	μm
T _{amb}	Operating temperature	-40	-	+85	°C

Features

- Up to 70 mm maximum measuring length
- Absolute position detection
- Effective resolution of 0.1 µm
- Absolute error smaller 30 µm
- Calibration algorithms
- True-power-on
- Wide temperature range from -40°C up to +85°C
- SPI protocol interfaces

Advantages

- Compact design
- High accuracy
- Robust and reliable

Applications

- Motion control systems
- Industrial automation
- Robotics applications
- Precision measurement







Mechanical Data 1)

T_{amb} = 25°C; unless otherwise specified

Symbol	Parameter	Conditions	min.	typ.	max.	Unit
L	Length of the module		-	25.0	-	mm
W	Width of the module		-	6.0	-	mm
Н	Height of the module		-	4.15	-	mm

¹⁾ more details in Fig. 2

Electrical Data

 $T_{amb} = 25$ °C, $V_{CC} = 3.3$ V; unless otherwise specified

Symbol	Parameter	Conditions	min.	typ.	max.	Unit
V _{cc}	Supply voltage		-	3.3	-	V
F _{Pos}	Position Refresh Rate		-	2.0	-	kHz
I _A	Current	V _{CC} = 5.0 V	100.0	125.0	150.0	mA
t _{Start}	Start time		-	100.0	-	ms
T _{op}	Operating temperature		-40	-	+85	°C
T _{storage}	Storage temperature		-40	-	+85	°C

Performance Data

 T_{amb} = +25°C, V_{CC} = 3.3 V, unless otherwise specified

Symbol	Parameter	Comment	Min.	Тур.	Max.	Unit
Acc	Accuracy		-	-	±30.0	μm
Rep	Repeatability		-	±0.5	-	μm
Res	Resolution		-	12	-	bit
Res _{eff}	Effective resolution		-	0.1	-	μm
Range	Measuring range	Bidirectional 3 cycles	5	-	70	mm
S	Speed		-	-	2	m/s

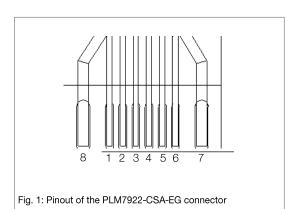
Environmental Data

Symbol	Conditions	min.	typ.	max.	Unit
Vibration resistance		-	-	785	m/s²
Shock resistance		-	-	980	m/s²
External magnetic field (thickness direction)		-	-	±100	mT
External magnetic field (moving direction)		-	-	±5	mT
External magnetic field (width direction)	no negative field allo- wed	-	-	+5	mT
Humidity		-	-	70	%

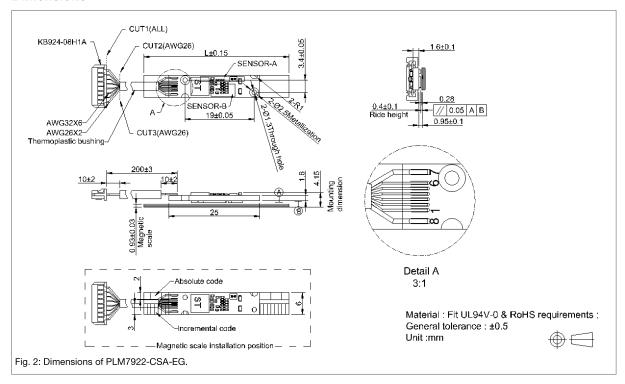


Pinout of the sensor module

Pad	Symbol	Parameter
1	SCK	Inner shield
2	Miso	Clock signal
3	Mosi	Inverted clock signal
4	CS	Supply Voltage
5	GND	Ground
6	V _{CC}	Supply Voltage
7	NC	Not connected
8	NC	Not connected

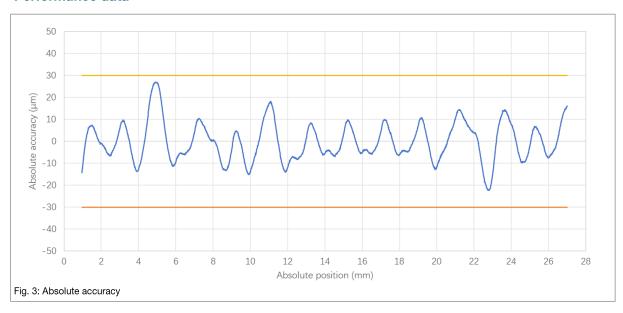


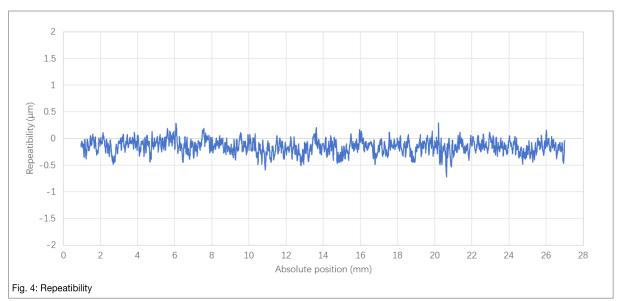
Dimensions





Performance data







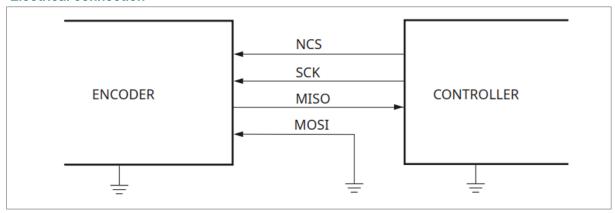
SPI interface

The Serial Peripheral Interface (SPI) bus is a four-wire bidirectional synchronous serial communication interface, typically used for short distance communication. It operates in full duplex mode, where master (controller) selects the slave with NCS line, generates clock signal on SCK line, sends command over MOSI line and receives data over MISO line.

All data signals are 3.3 V LVTTL. Inputs are 5 V tolerant. The maximum current sourced or sunk from signal lines should not exceed 5 mA. Single-ended signals should be as short as possible, especially when high frequencies are used.

Signal termination: 100Ω resistors are added in series with all SPI signals.

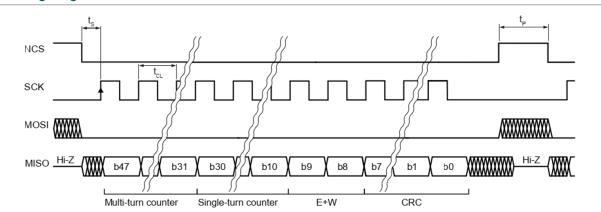
Electrical connection



Signal	Description
NCS	Active low. NCS line is used for synchronization between master and slave devices. During communication it must be held low. Idle is high. When NCS is high, MISO line is in high-Z mode. This allows connection of multiple slaves in paralell, sharing all lines except NCS. Encoder position data is latched on falling edge of NCS signal.
SCK	Serial clock. Shifts out the data on rising edge.
MOSI	Master output Slave input. Command from the controller to encoder. If only position data is requested, this signal should always be zero. It can be tied to GND.
	Master input Slave output. Data is output on rising edge on SCK after NCS low. When NCS is high, MISO line is in high-Z mode.



Timing diagram



The controller starts the communication by setting the NCS signal low. At the same time the encoder position is latched. A delay of $t_{\rm S}$ is required to allow the encoder to prepare the data which is then shifted to MISO output on rising edges of clock signal SCK.

Encoder Position and General Status (active low) data is transmitted, followed by CRC (inverted) of the entire data packet.

Communication parameters

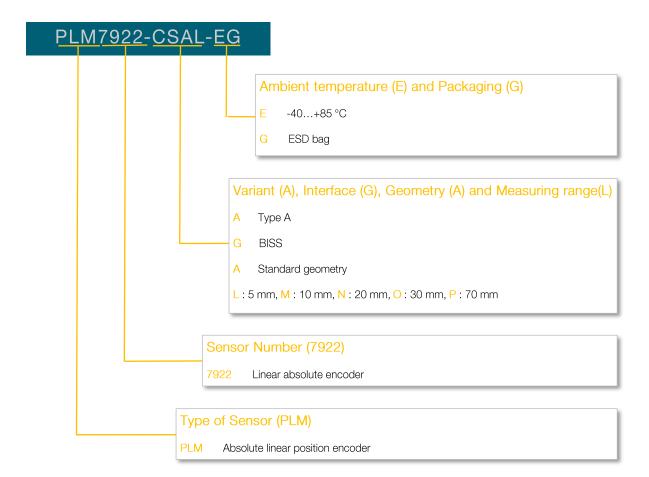
Parameter	Symbol	Min	Max
Clock period	t _{CL}	250 ns	-
Clock frequency	f _{CL}	-	4 MHz
Time after NCS low to first SCK rising edge	t _s	5 µs	-
Pause time	t₽	5 µs	-

Encoder position data structure

Bit	Description			
b31:b10	Encoder position + zero padding bits – Left aligned, MSB first.			
b9	Error – If low, the position data is not valid.			
b8	Warning – If low, the position data is valid, but some operating conditions are close to limits.			
b7:b0	Inverted CRC, 0x97 polynomial			



Additional Information on Ordering Code





General Information

Product Status

Article	Status
PLM7922-CSA-EG	The product is under development.
Note	The status of the product may have changed since this data sheet was published. The latest information is available on the internet at www.sensitec.com.

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Application Information

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Changelist

Version	Description of the Change	Date
PLM7922C.DSE.00	Original (pp. 1-9)	04/2025

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